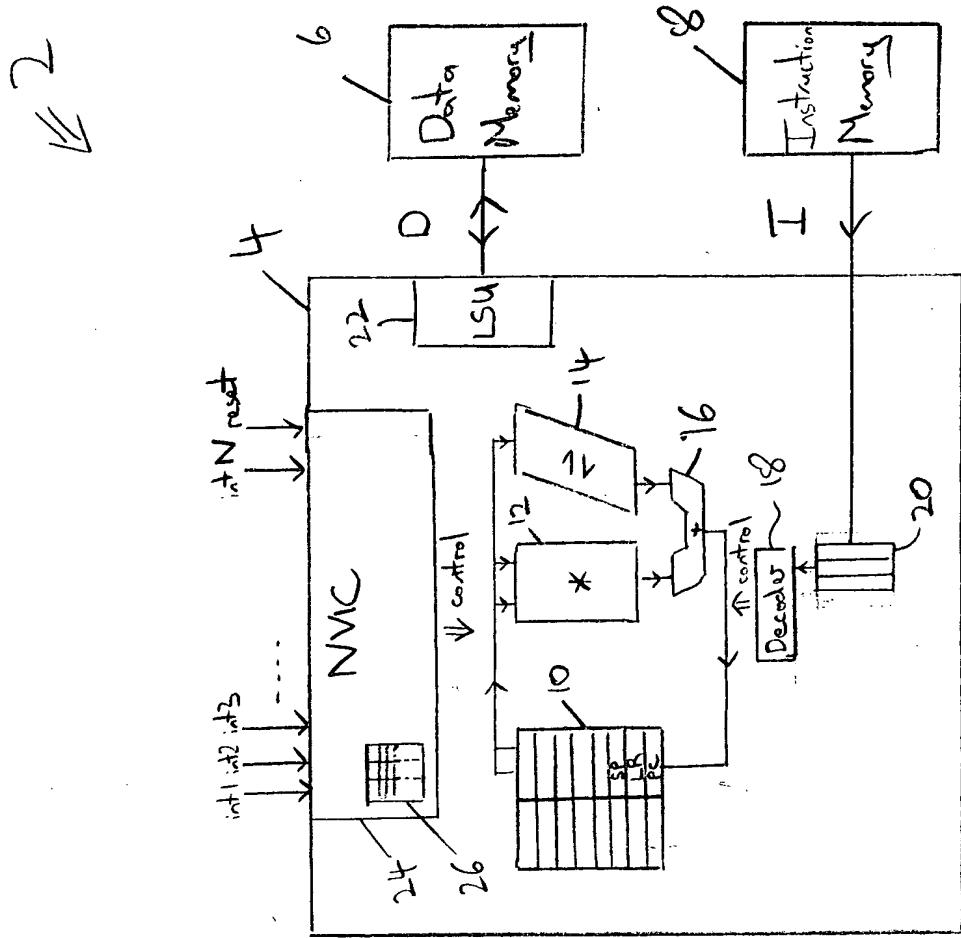


Fig. 1



# NVIC Control Table

	Pending	Active	Priority	pre-emption	ordering
Int 0		1		↔	→
Int 1				↓	
Int 2	1			↓	
Int 3		1		↓	
⋮					

Fig. 2

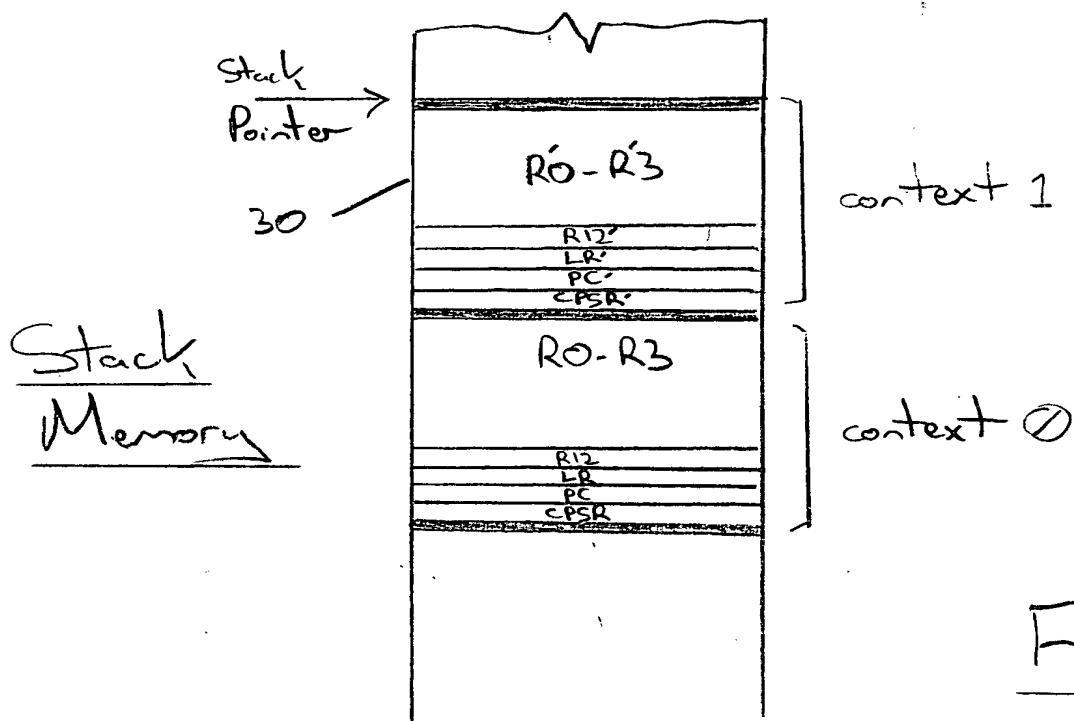
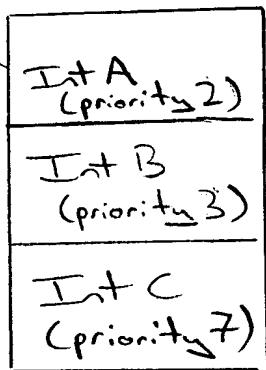


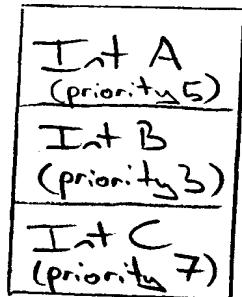
Fig. 3

## Priority Inversions

32

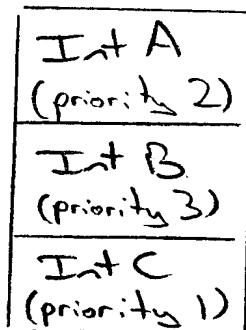


Int A  
priority  
change



Stack priority = 3

Int C  
priority  
change



Stack priority = 1

Fig. 4

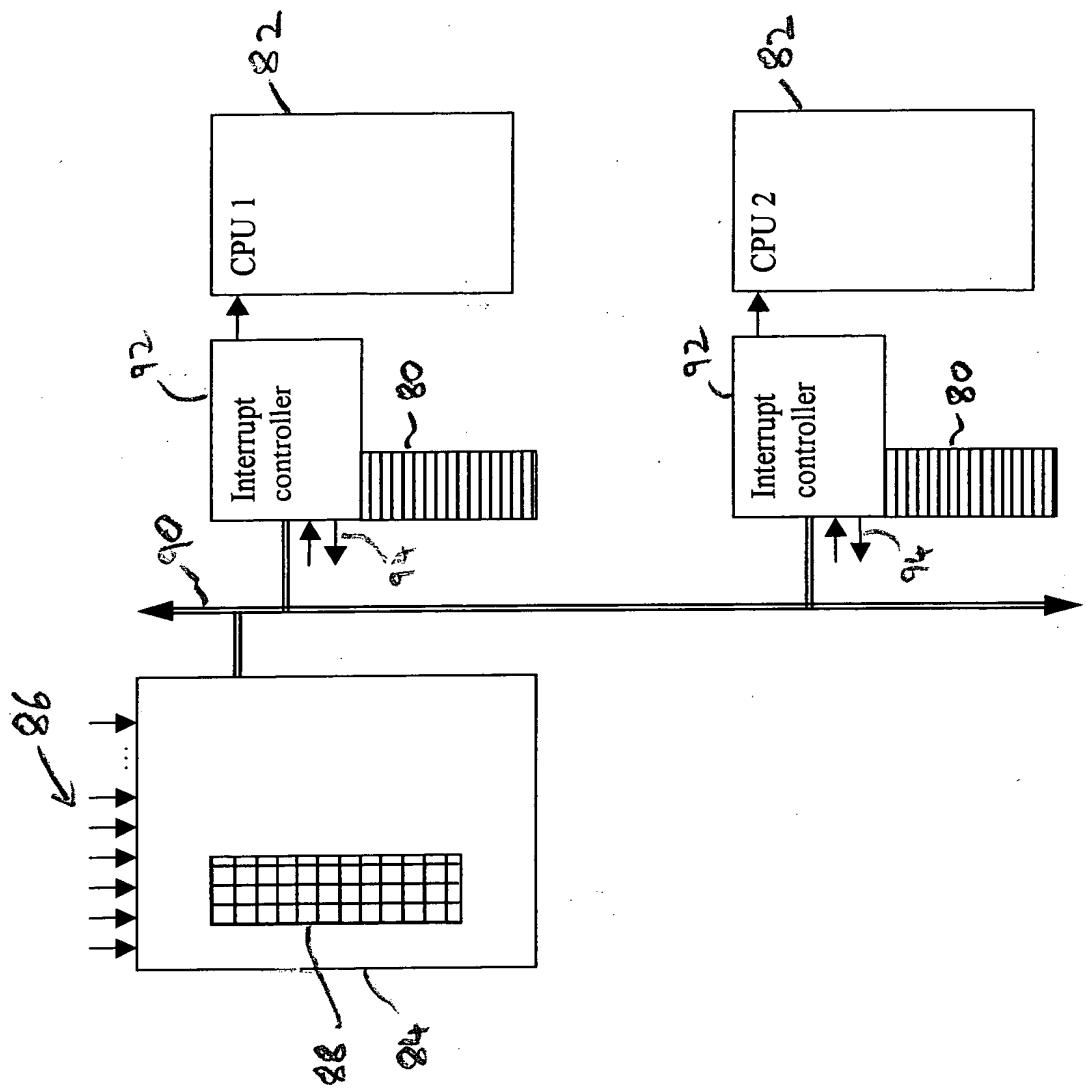


Fig. 5

# Main Flow

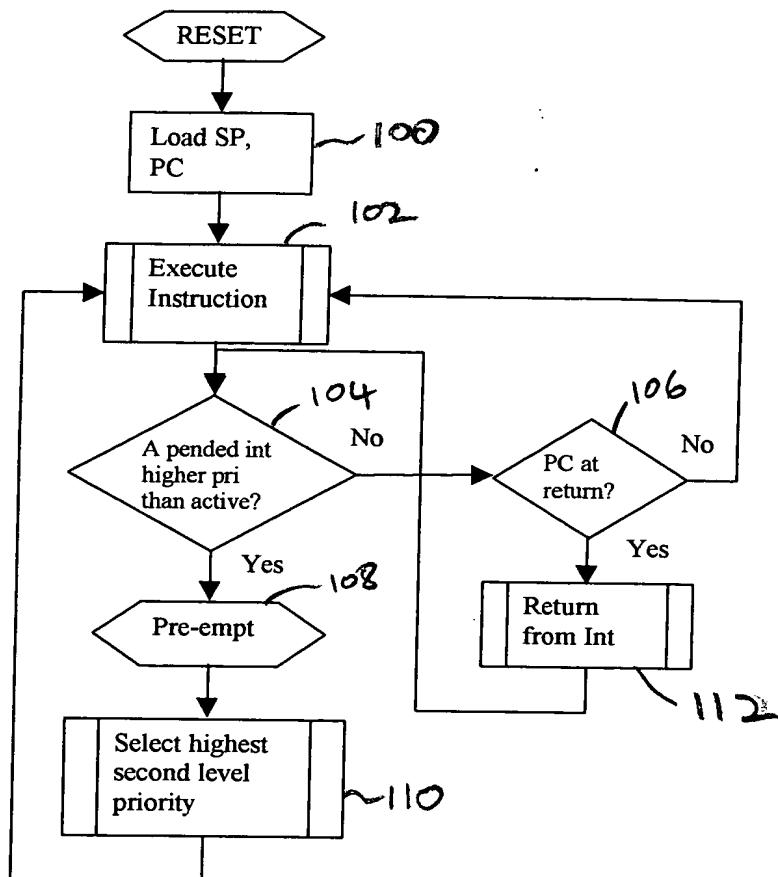


Fig. 6

# Main Flow with Tail Chaining

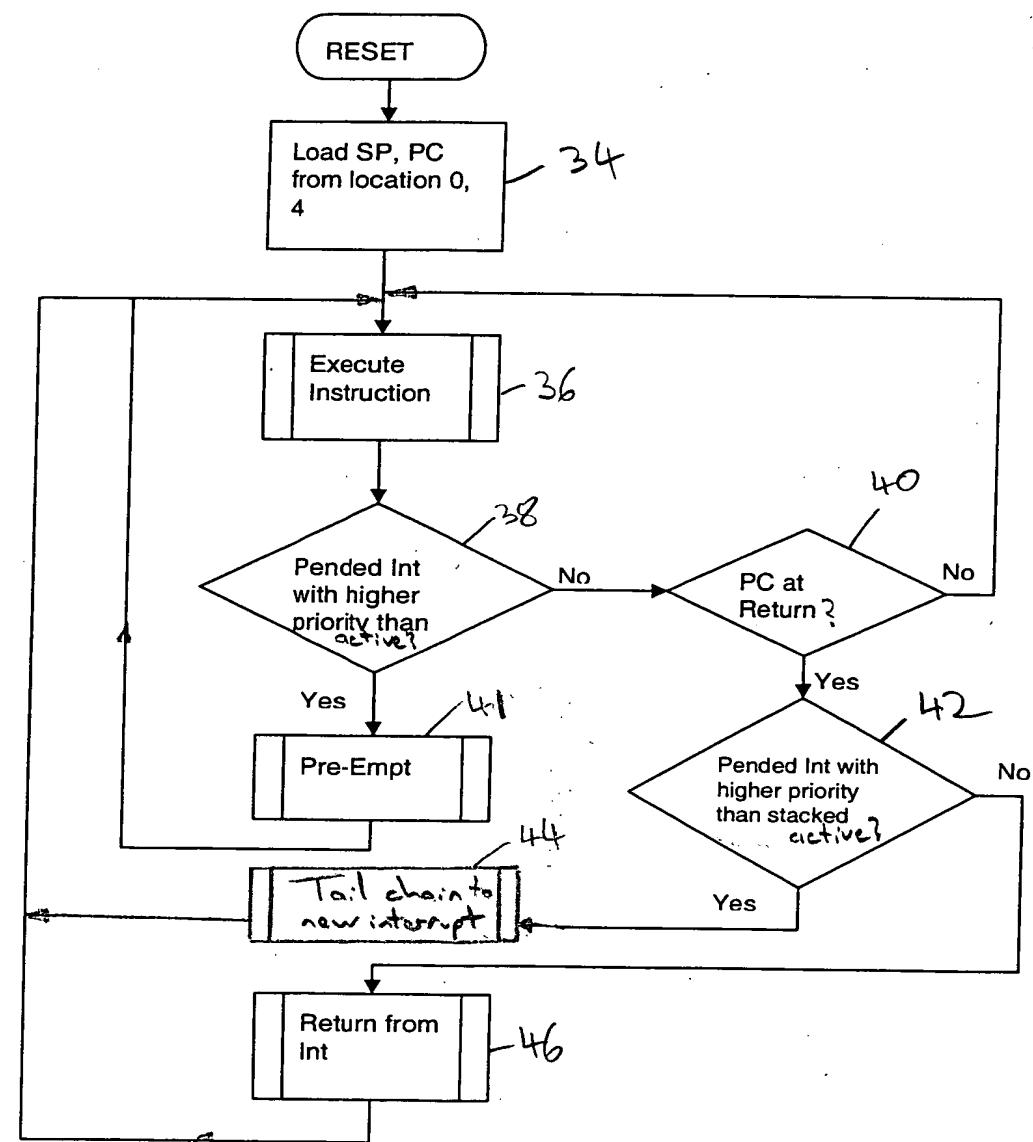


Fig. 7

# Pre-emption Flow

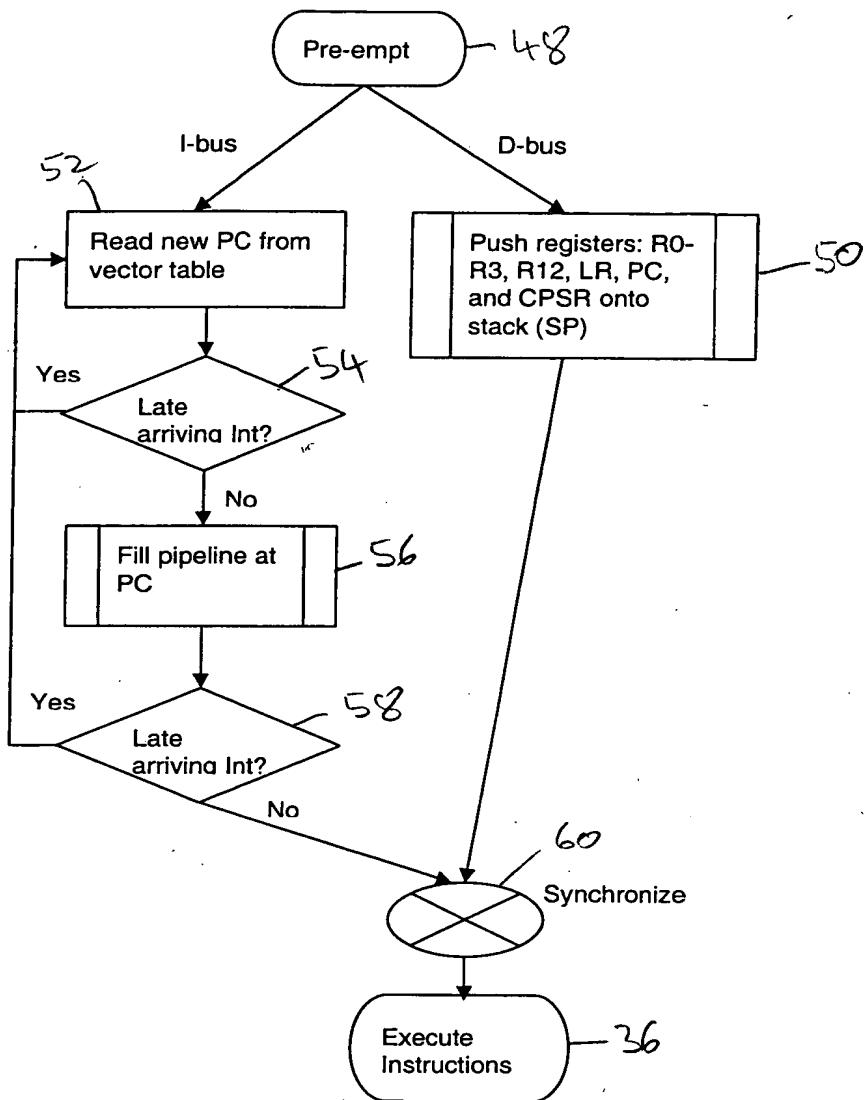


Fig. 8

# Return From Interrupt Flow

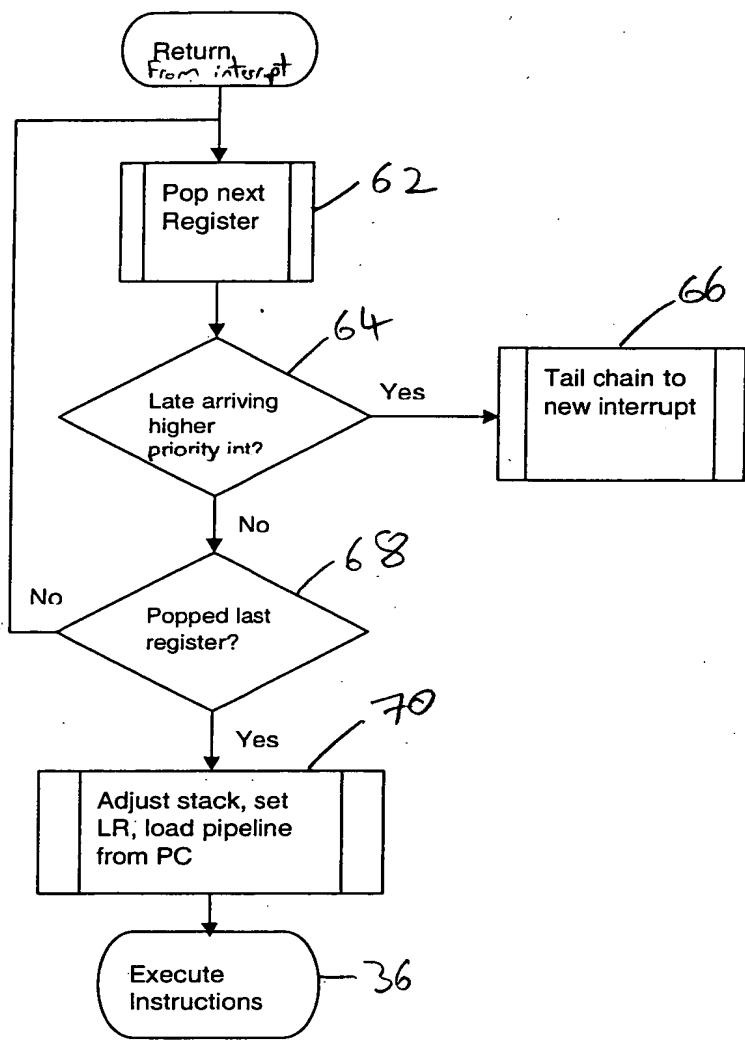
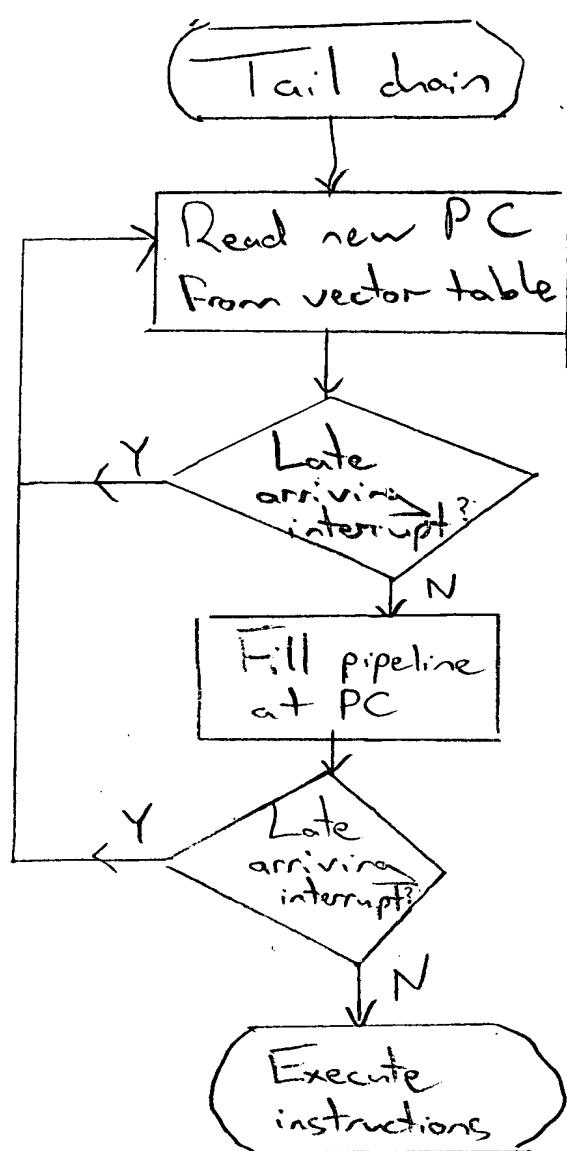


Fig. 9



Tail Chaining  
Flow

Fig. 10